

REMARKS

The Examiner has required Applicant to restrict this application to one of the following inventions:

- I. Claims 1, 5, and 6, drawn to an integrated circuit structure, which is device claimed, classified in class 716, subclass 1, or
- II. Claims 7-12, drawn to a method for designing an integrated circuit layout, classified in class 716, subclass 11.


Applicant hereby selects invention II, set forth in claims 7-12, and drawn to a method for designing an integrated circuit layout, classified in class 716, subclass 11. Applicant reserves the right to submit any Divisional patent applications directed toward the non-selected invention.

An early action on the merits of this application is respectfully requested. Should any points remain in issue, which the Examiner feels could best be resolved by either a personal or a telephone interview, it is urged that Applicant's local attorney be contacted at the exchange listed below.

Respectfully submitted,

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By:


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